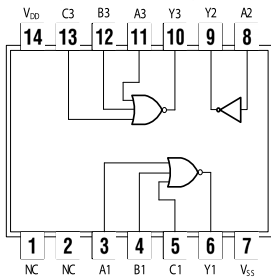


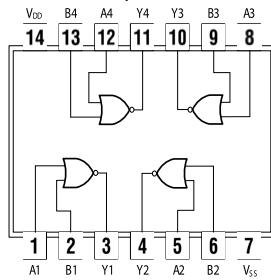
# CMOS IC 핀 배치도



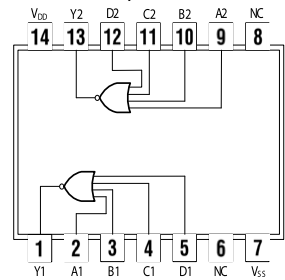
**4000**  
Dual 3-Input NOR Gate plus Inverter



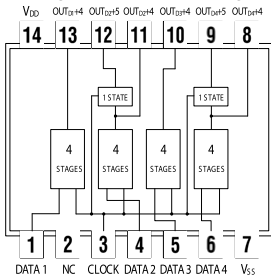
**4001**  
Quad 2-Input NOR Gate



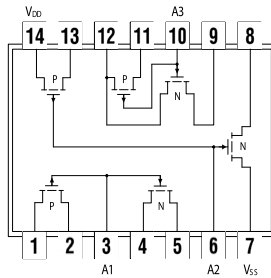
**4002**  
Dual 4-Input NOR Gate



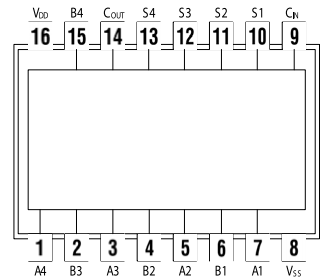
**4006**  
18-Stage Static Shift Register



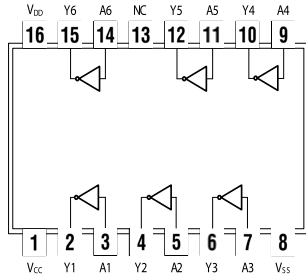
**4007**  
Dual Complementary Pair plus Inverter



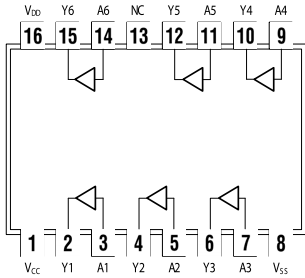
**4008**  
4-Bit Full Adder with Parallel Carry Out



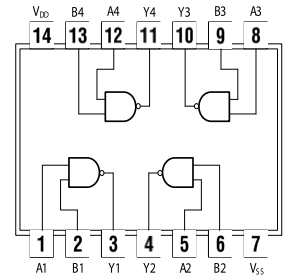
**4009**  
Hex Buffer/Converter(Inverting)



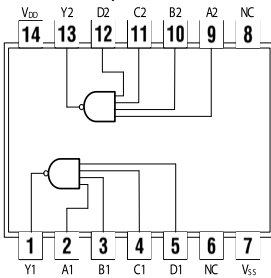
**4010, 4050**  
Hex Buffer/Converter(Non-Inverting)



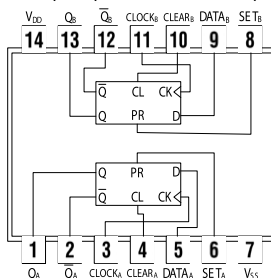
**4011**  
Quad. 2-Input NAND Gate



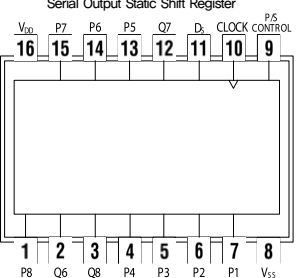
**4012**  
Dual 4-Input NAND Gate

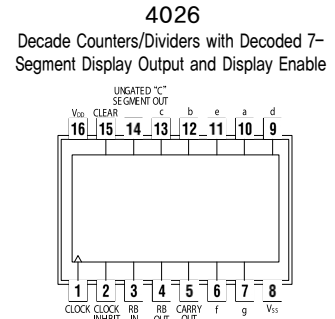
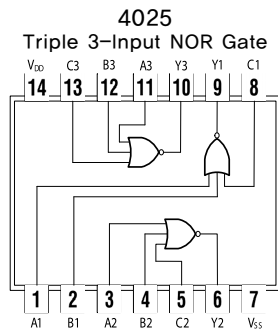
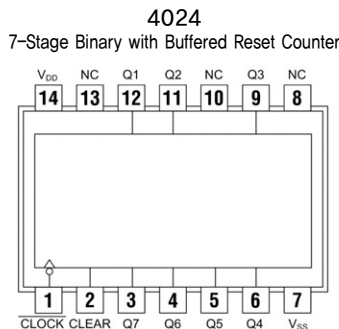
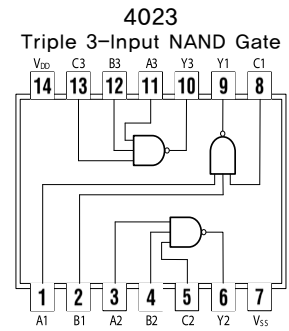
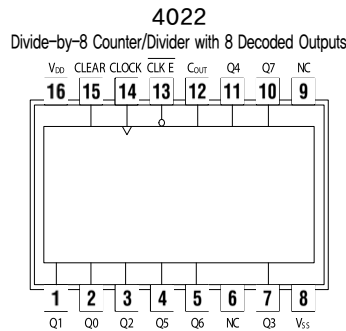
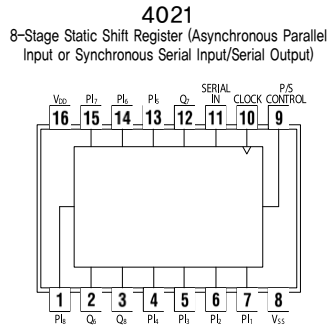
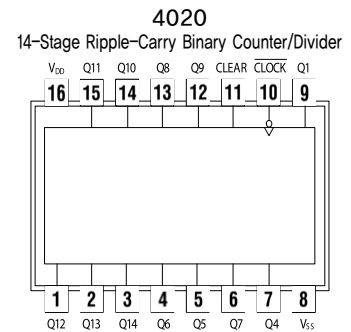
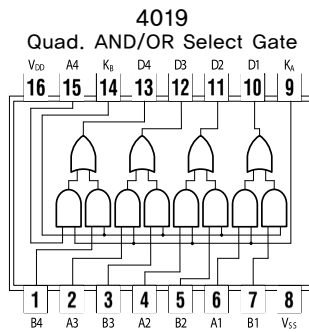
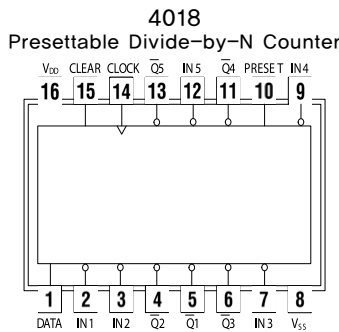
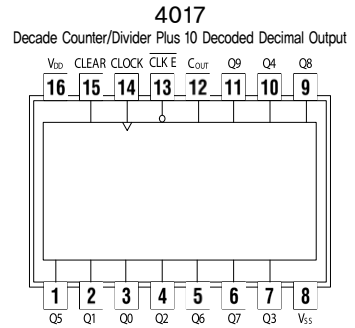
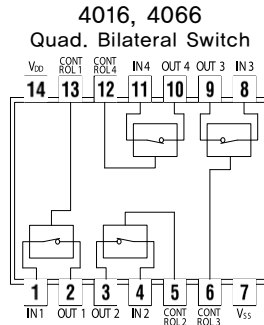
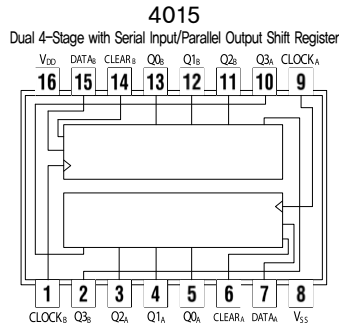


**4013**  
Dual D Flip Flop with Set/Reset Capability

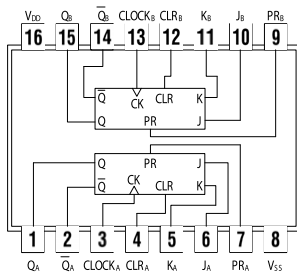


**4014**  
8-Stage with Synchronous Parallel Input/  
Serial Output Static Shift Register

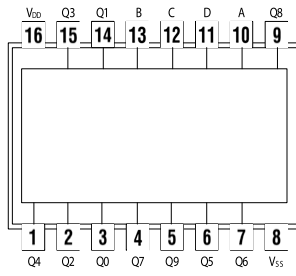




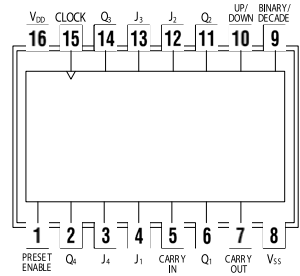
**4027**  
Dual J-K Master-Slave Flip Flop



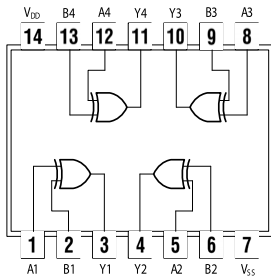
**4028**  
BCD-to-Decimal/Binary-to-Octal Decoder



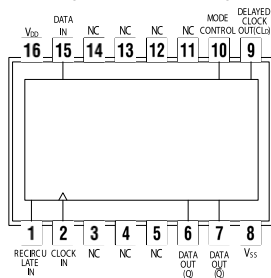
**4029**  
Presettable Binary/BCD Up/Down Counter



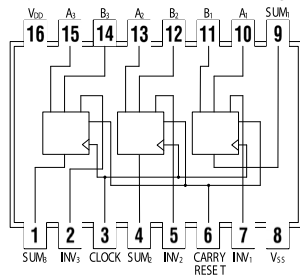
**4030**  
Quad. Exclusive-OR Gate



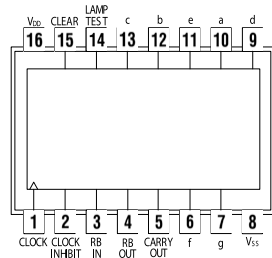
**4031**  
64-Stage Static Shift Register



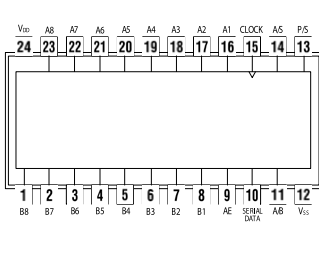
**4032, 4038**  
Triple Serial Adder (Positive Logic)



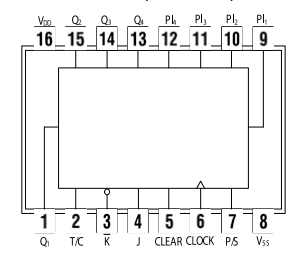
**4033**  
Decade Counters/Dividers with 7-Segment Display Output and Ripple Blanking



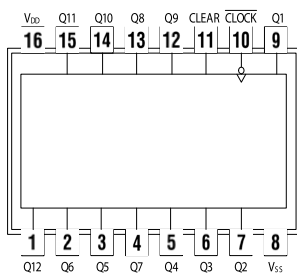
**4034**  
8-Stage Static Bidirectional Parallel/Serial Input/Output Bus Register



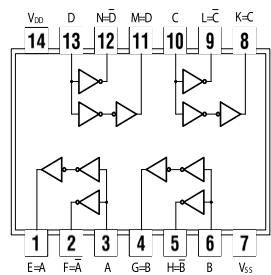
**4035**  
4-Stage Parallel-In/Parallel-Out with J-K Input and True Complement Output



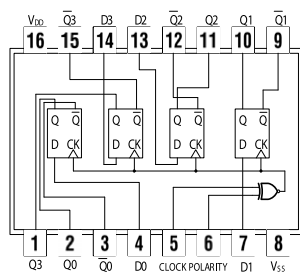
**4040**  
12-Stage Ripple-Carry Binary Counter/Divider



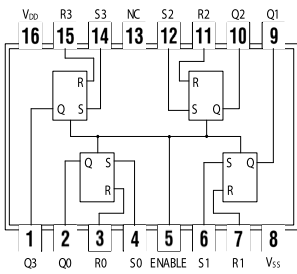
**4041**  
Quad. True/Complement Buffer



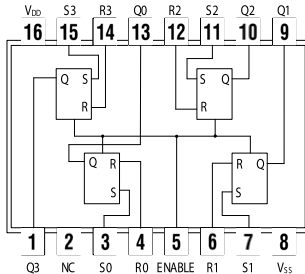
**4042**  
Quad. Clocked D Latch



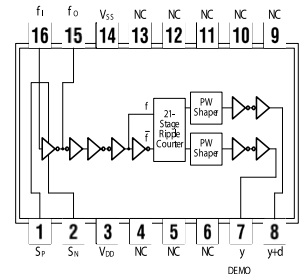
**4043**  
Quad. NOR R-S Latch (3-State Output)



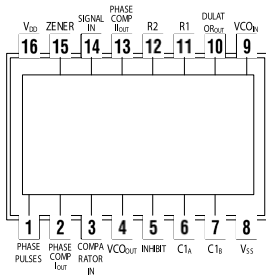
**4044**  
Quad. NAND R-S Latch (3-State Output)



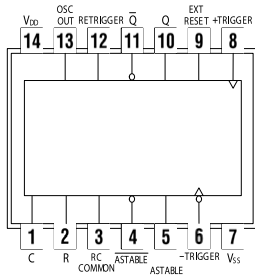
**4045**  
21-Stage Counter



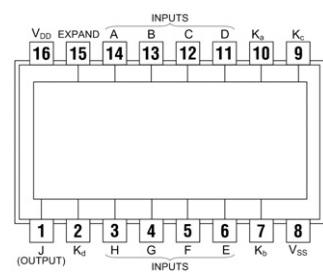
**4046**  
Phase-Locked Loop



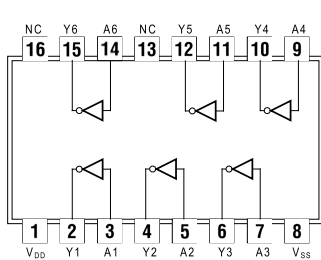
**4047**  
Low-Power Monostable/Astable Multivibrator



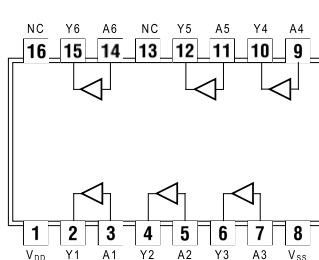
**4048**  
Multi-Function Expandable 8-Input Gate



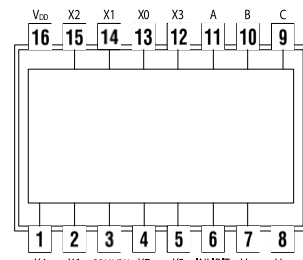
**4049**  
Hex Buffer/Converter (Inverting)



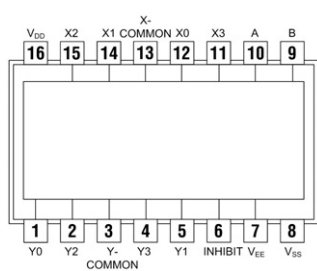
**4050**  
Hex Buffer/Converter(Non-Inverting)



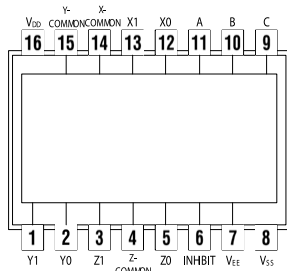
**4051**  
8-Channel Analog Multiplexer/Demultiplexer



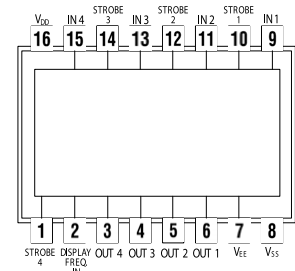
**4052**  
Dual 4-Channel Analog Multiplexer/Demultiplexer



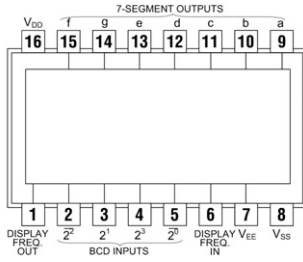
**4053**  
Triple 2-Channel Analog Multiplexer/Demultiplexer



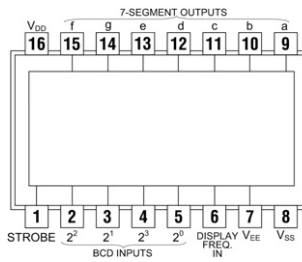
**4054**  
4-Line Liquid-Crystal Display Drivers



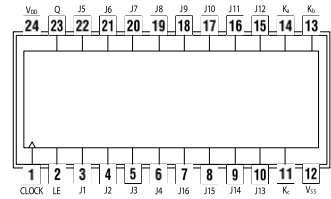
**4055**  
Liquid-Crystal BCD-to-7 Segment Decoder/Driver  
with Display-Frequency Outputs



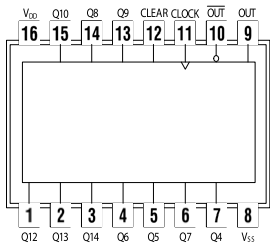
**4056**  
Liquid-Crystal BCD-to-7 Segment Decoder/Driver  
with Strobed-Latch Function



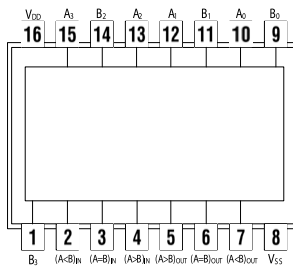
**4059**  
Programmable Divide-By-N Counter



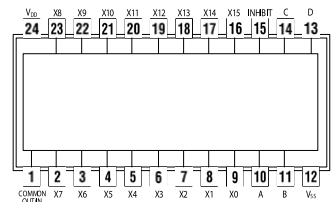
**4060**  
14-Stage Ripple-Carry Binary Counter/Divider and  
Oscillator



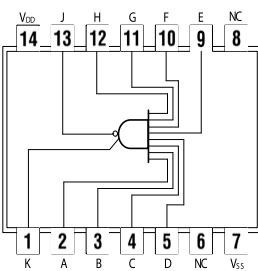
**4063**  
4-Bit Magnitude Comparator



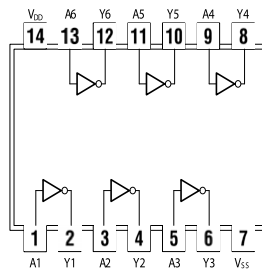
**4067**  
16-Channel Analog Multiplexer/Demultiplexer



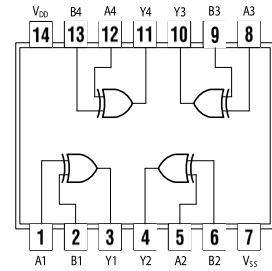
**4068**  
8-Input NAND/AND Gate



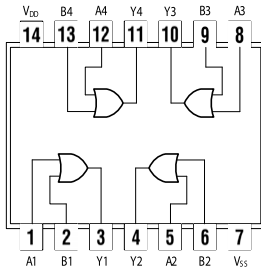
**4069**  
Hex Inverter



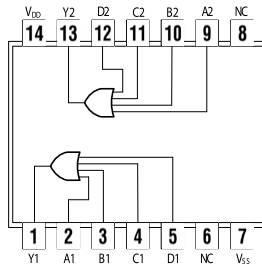
**4070**  
Quad. Exclusive OR Gate



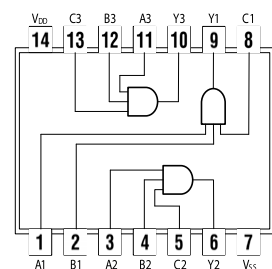
**4071**  
Quad. 2-Input OR Gate

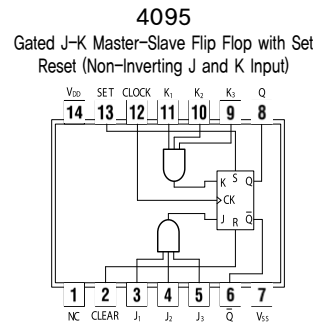
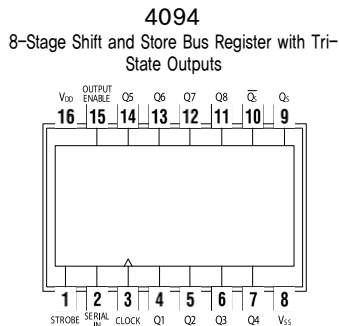
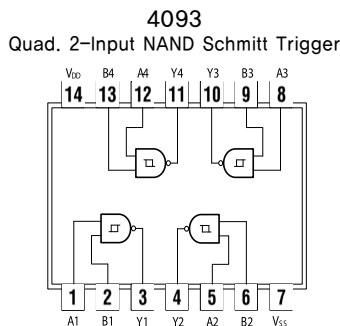
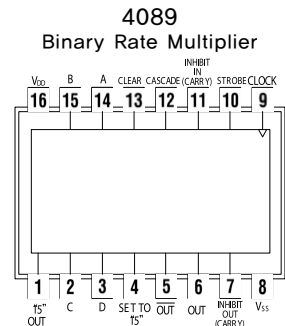
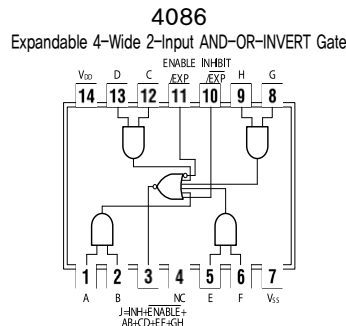
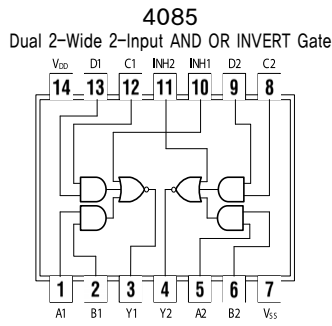
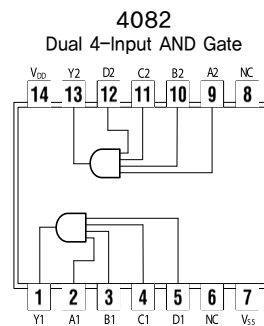
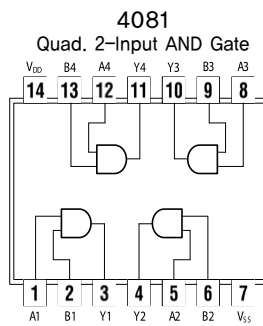
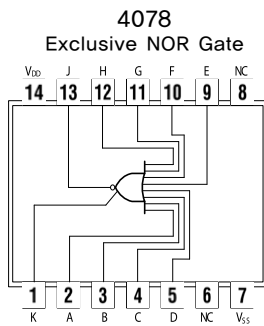
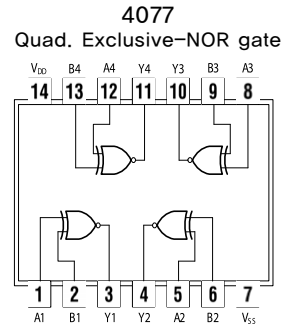
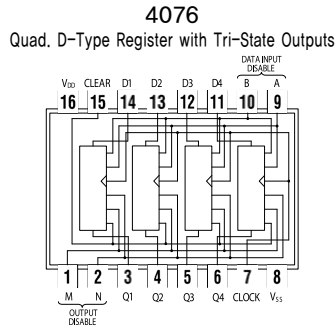
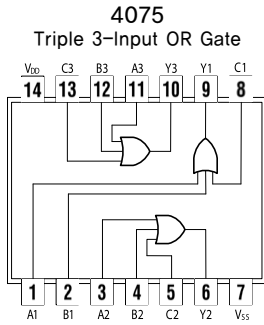


**4072**  
Dual 4-Input OR Gate



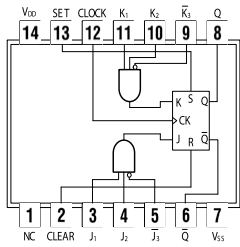
**4073**  
Triple 3-Input AND Gate





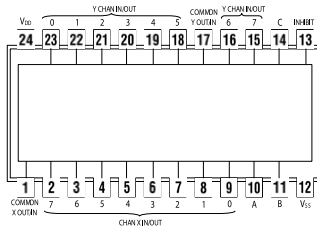
**4096**

Gated J-K Master-Slave Flip Flop with Set Reset  
(Inverting and Non-Inverting J and K Input)



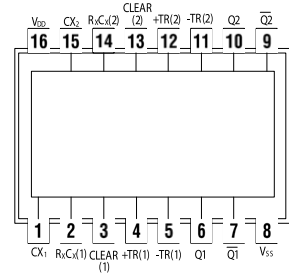
**4097**

Analog Multiplexers/Demultiplexers



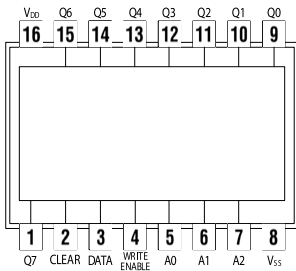
**4098**

Dual Monostable Multivibrator



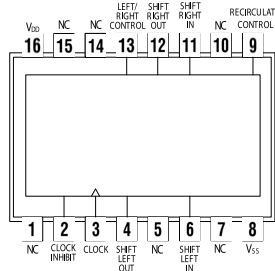
**4099**

8-Bit Addressable Latch



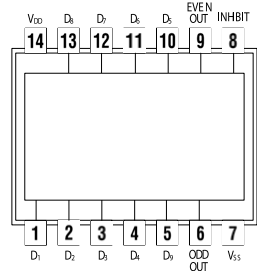
**40100**

32-Stage Static Left/Right Shift Register



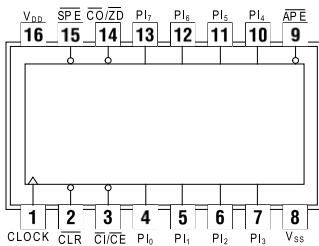
**40101**

9-Bit Parity Generator/Checker



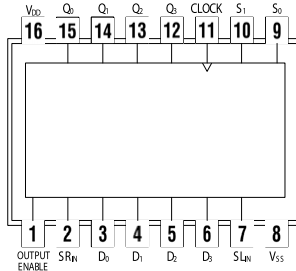
**40102, 40103**

8-Stage Presettable Synchronous BCD Down Counter



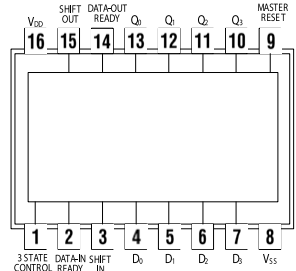
**40104**

4-Bit Bidirectional Universal Register



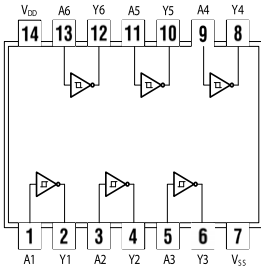
**40105**

4-Bit Wide x 16-Bit Long FIFO Register



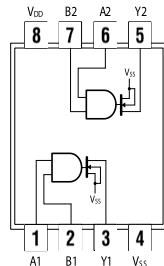
**40106**

Hex Schmitt Trigger



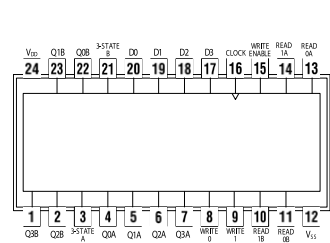
**40107**

Dual 2-Input NAND Buffer/Driver



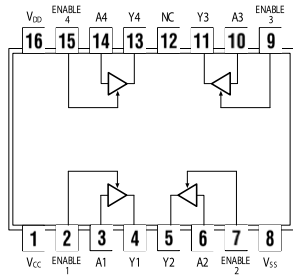
**40108**

4x4 Multiport Register

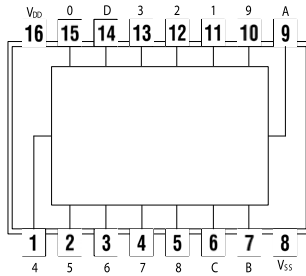




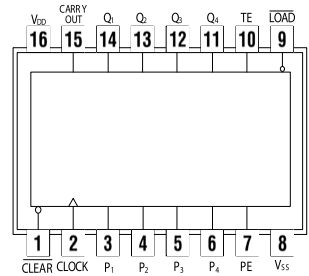
**40109**  
Quad. Low-to-High Voltage Level Shifter



**40147**  
10-Line to 4-Line BCD Priority Encoder



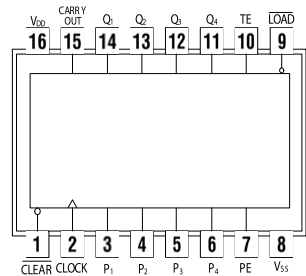
**40160**  
Decade Counter with Asynchronous Clear



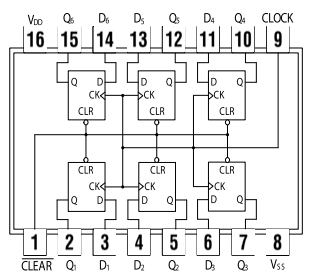
**40161**  
4-Bit Binary Counter with Asynchronous Clear

**40162**  
Decade Counter with Synchronous Clear

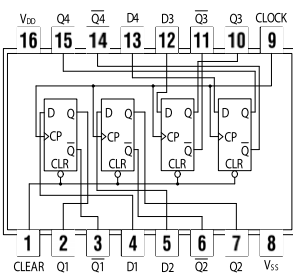
**40163**  
4-Bit Binary Counter with Synchronous Clear



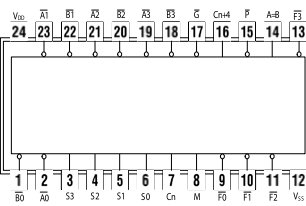
**40174**  
Hex D Flip Flop



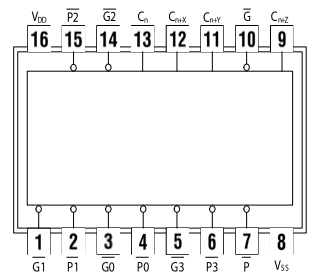
**40175**  
Quad. D Flip Flop



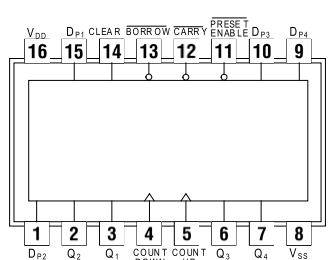
**40181**  
4-Bit Arithmetic Logic Unit



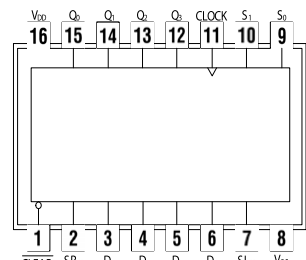
**40182**  
Look-Ahead Carry Generator



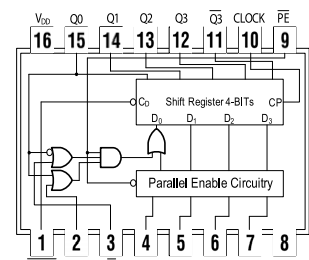
**40192(BCD), 40193(BIN)**  
Presettable 4-Bit BCD(BINARY) Up/Down Counter



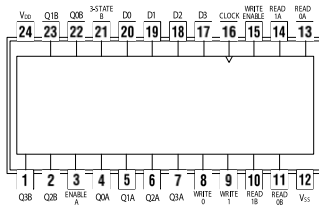
**40194**  
4-Bit Bidirectional Universal Shift Register



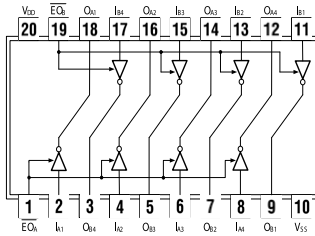
**40195**  
4-Bit Parallel Shift Register



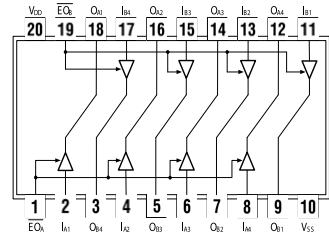
**40208**  
4x4 Multiport Register



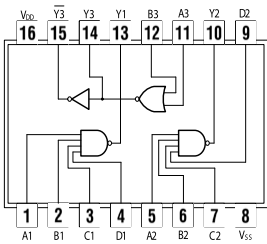
**40240**  
Inverting Octal 3-State Buffer



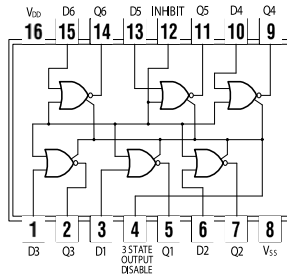
**40244**  
Octal 3-State Buffer



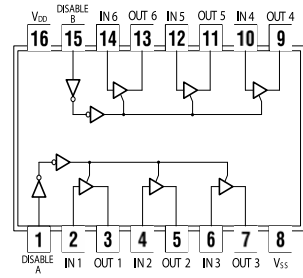
**4501**  
Triple Gate(Dual 4-Input NAND, 2-Input NOR/OR, 8-Input AND/NAND)



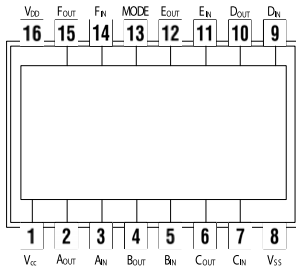
**4502**  
Strobed Hex Inverter/Buffer



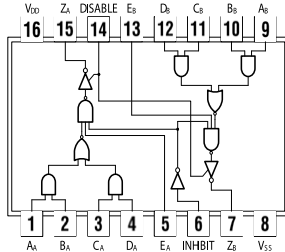
**4503**  
Hex Non-Inverting 3-State Buffer



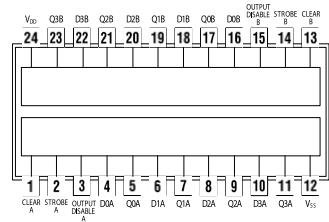
**4504**  
Hex Level Shifter for TTL or CMOS to CMOS



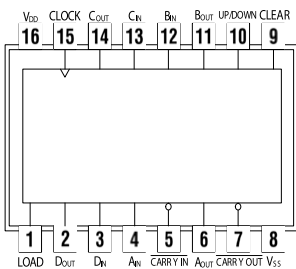
**4506**  
Dual 2-Wide 2-Input Expandable AND-OR-INVERTER Gate



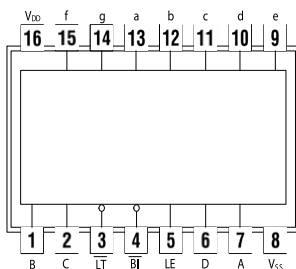
**4508**  
Dual 4-Bit Latch



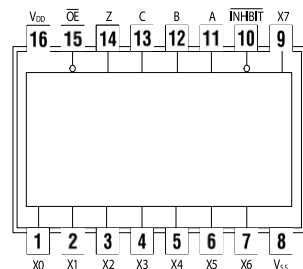
**4510(BCD), 4516(BIN)**  
BCD(BINARY) Up/Down Counter



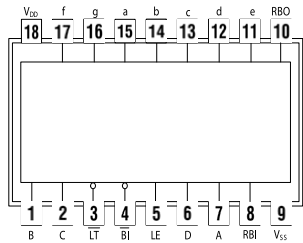
**4511**  
BCD-to-7-Segment Latch/Decoder/Driver



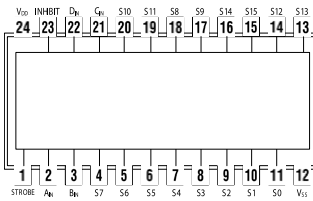
**4512**  
8-Channel Data Selector



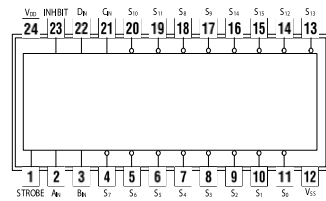
**4513**  
BCD-to-7-Segment Latch/Decoder/Driver with  
Ripple Blanking



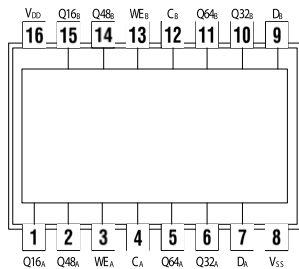
**4514**  
4-Bit Latch/4-to-16 Line Decoder(Active High)



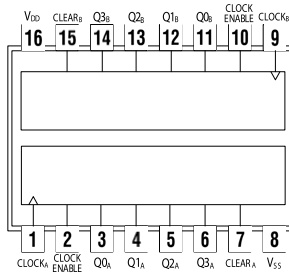
**4515**  
4-Bit Latch/4-to-16 Line Decoder(Active Low)



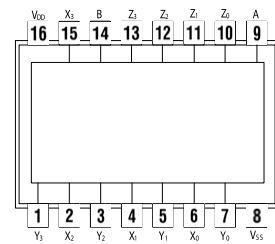
**4517**  
Dual 64-Bit Static Shift Register



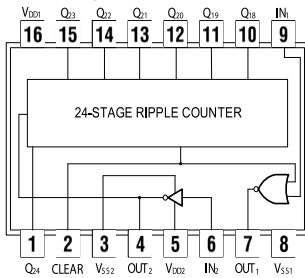
**4518(BCD), 4520(BIN)**  
Dual BCD(BINARY) Up Counter



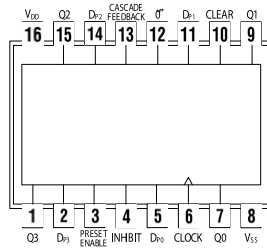
**4519**  
4-Bit AND/OR Selector or Quad, 2-Channel Data  
Selector or Quad Exclusive NOR Gate



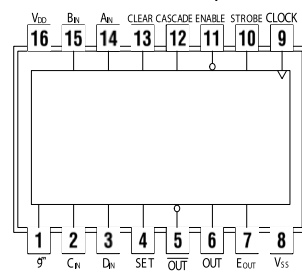
**4521**  
24-Stage Frequency Divider



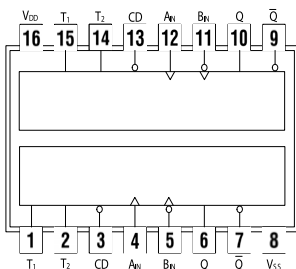
**4522(BCD), 4526(BIN)**  
Programmable Divide-by-N 4-Bit BCD(BINARY)  
Counter



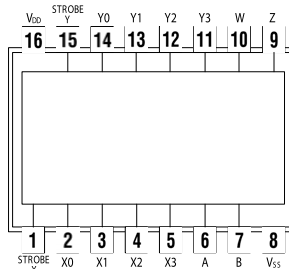
**4527**  
BCD Rate Multiplier



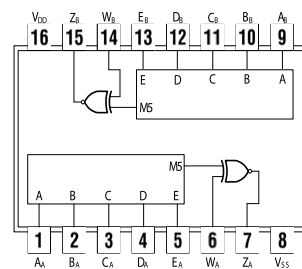
**4528**  
Dual Monostable Multivibrator

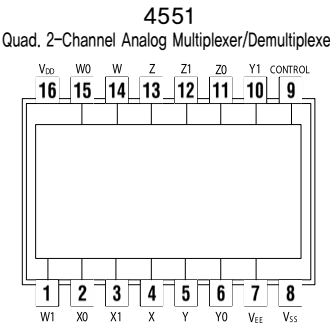
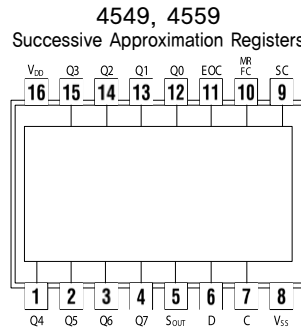
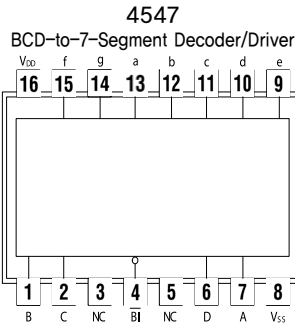
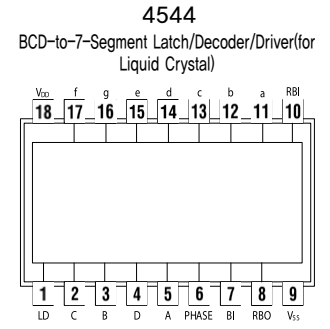
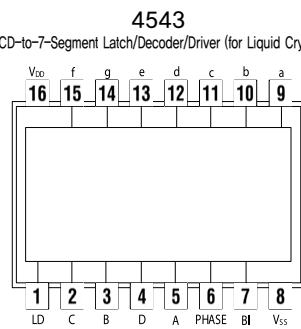
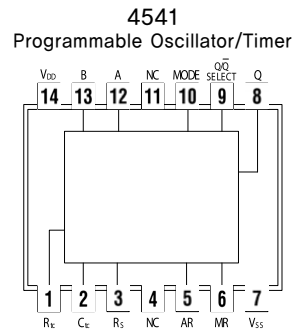
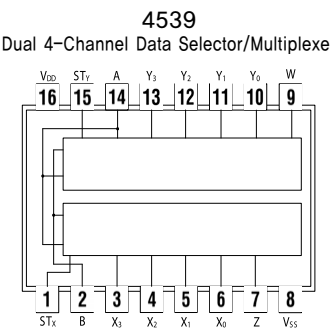
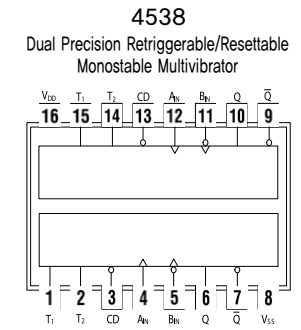
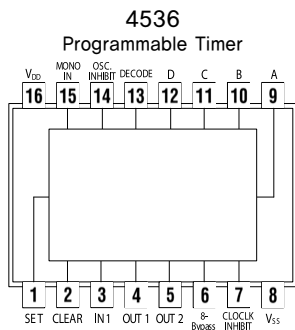
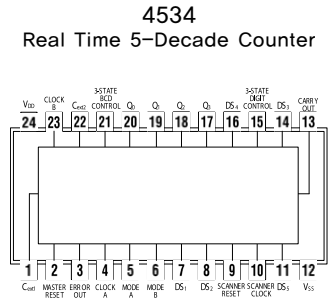
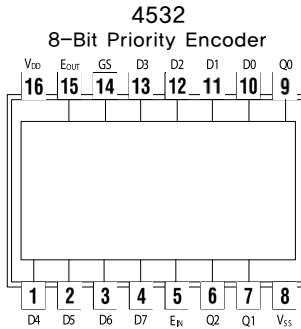
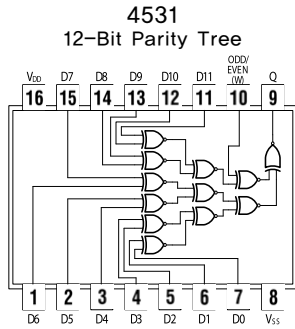


**4529**  
Dual 4-Channel Analog Data Selector

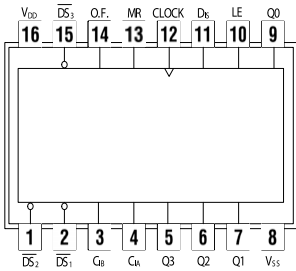


**4530**  
Dual 5-Input Majority Logic Gate

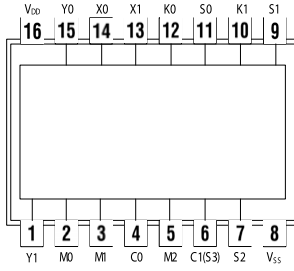




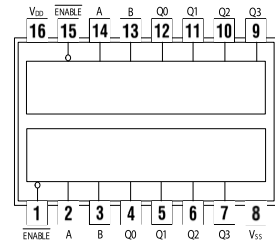
**4553**  
3-Digit BCD Counter



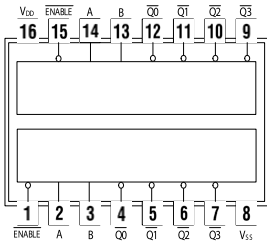
**4554**  
2x2 Bit Parallel Binary Multiplier



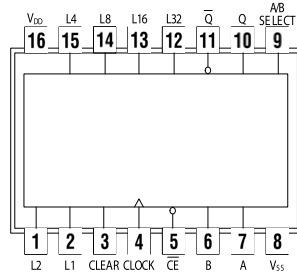
**4555**  
Dual Binary to 1-of-4 Decoder/Demultiplexer  
(Active High Outputs)



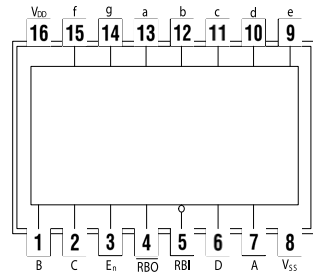
**4556**  
Dual Binary to 1-of-4 Decoder/Demultiplexer  
(Active High Outputs)



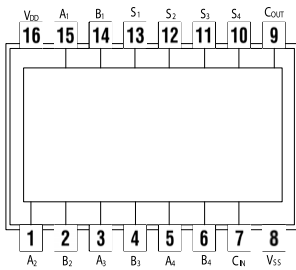
**4557**  
1-to-64 Bit Variable Length Shift Register



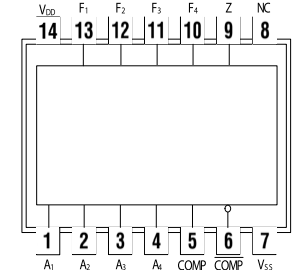
**4558**  
BCD-To-Seven Segment Decoder



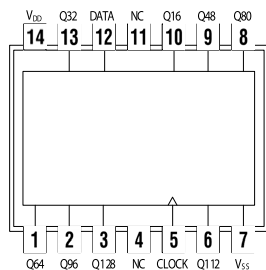
**4560**  
NBCD Adder



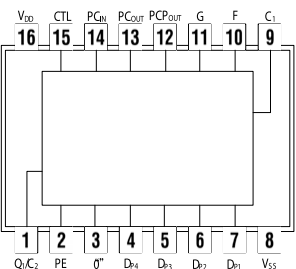
**4561**  
9's Complementer



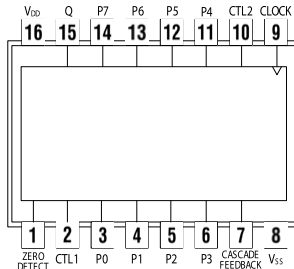
**4562**  
128-Bit Static Shift Register



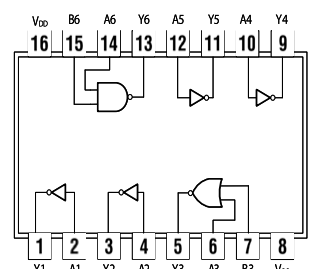
**4568**  
Phase Comparator/Programmable Counter



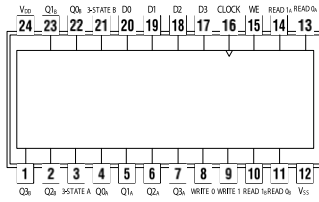
**4569**  
Programmable Divide-by-N Dual 4-Bit Binary/BCD Down Counter



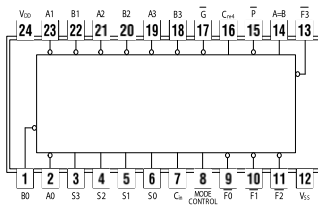
**4572**  
Hex Gate/4 Inverter plus 2-Input NOR plus 2-Input NAND (Gate)



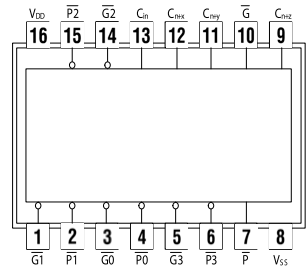
**4580**  
4x4 Multiport Register



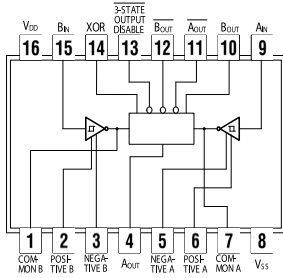
**4581**  
4-Bit Arithmetic Logic Unit



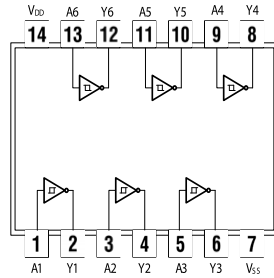
**4582**  
Look-Ahead Carry Block



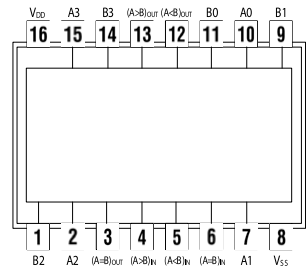
**4583**  
Dual Schmitt Trigger



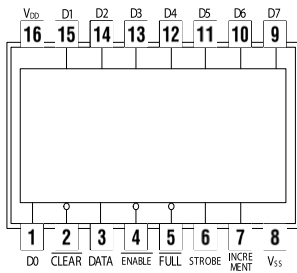
**4584**  
Hex Schmitt Trigger



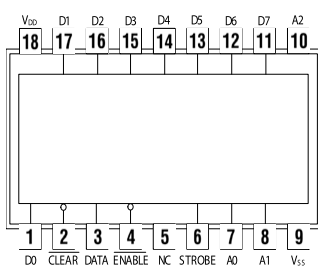
**4585**  
4-Bit Magnitude Comparator



**4597**  
8-Bit Bus-Compatible Counter Latch(3-State Output)



**4598**  
8-Bit Bus-Compatible Addressable Latch(3-State Output)



**4599**  
8-Bit Addressable Latch

